

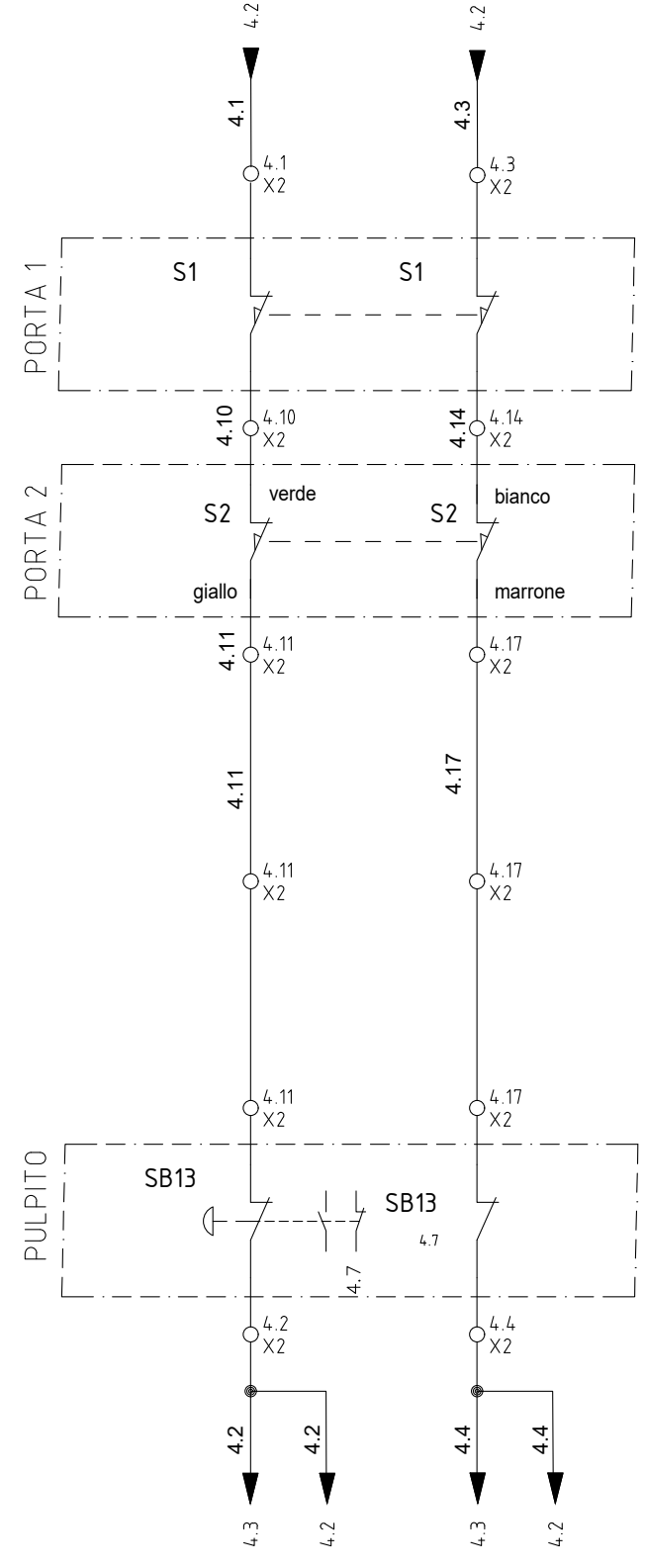
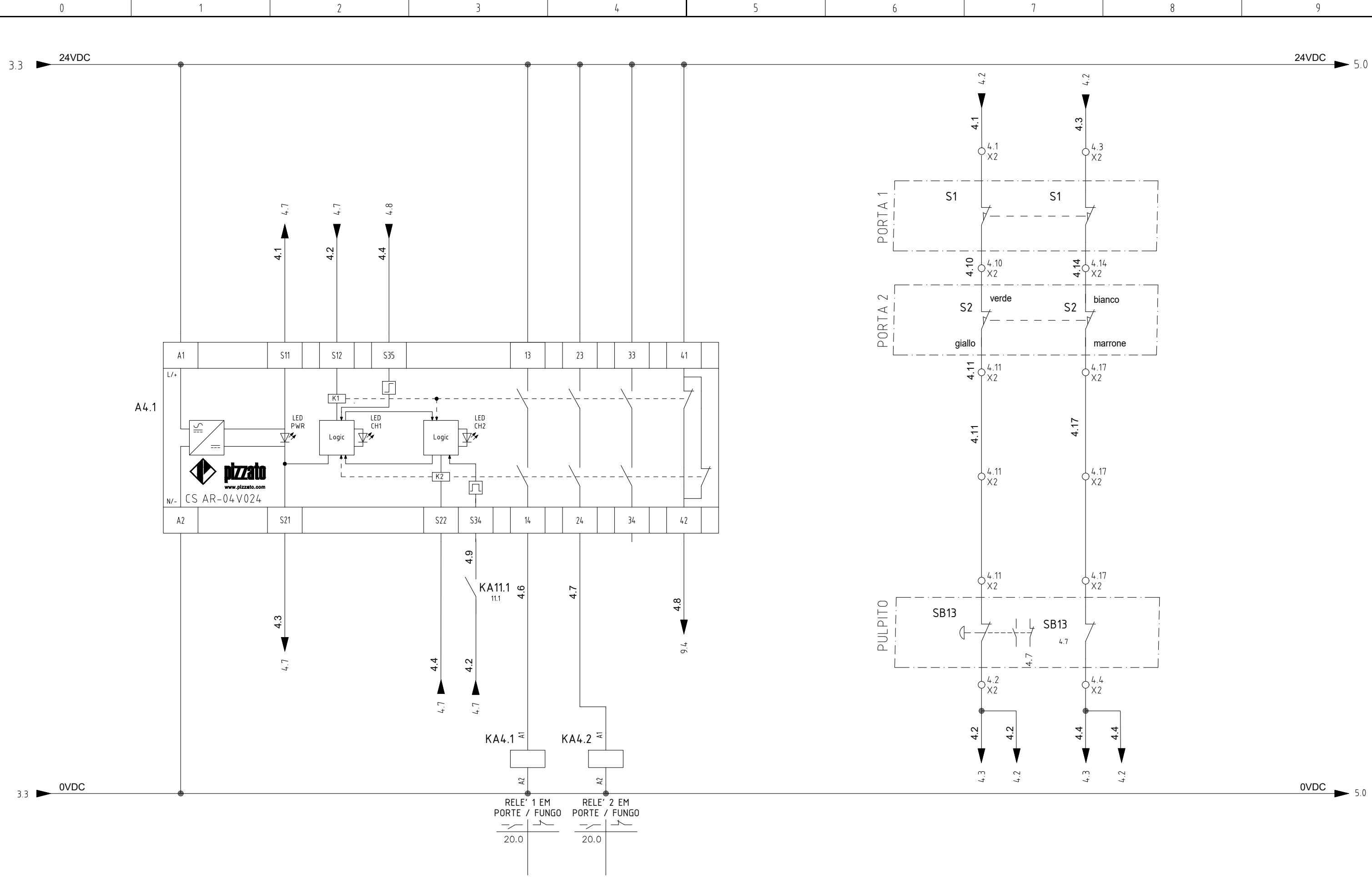
REV.	MODIFICA	DATA	FIRMA	APPR.	DATA	04/07/24
					DISEGN.	T.B.
					VISTO	



FILE :

PDL34
INGRESSO LINEA

E24Q103	=
	+
24Q103	FG. 3
	F.S. 4

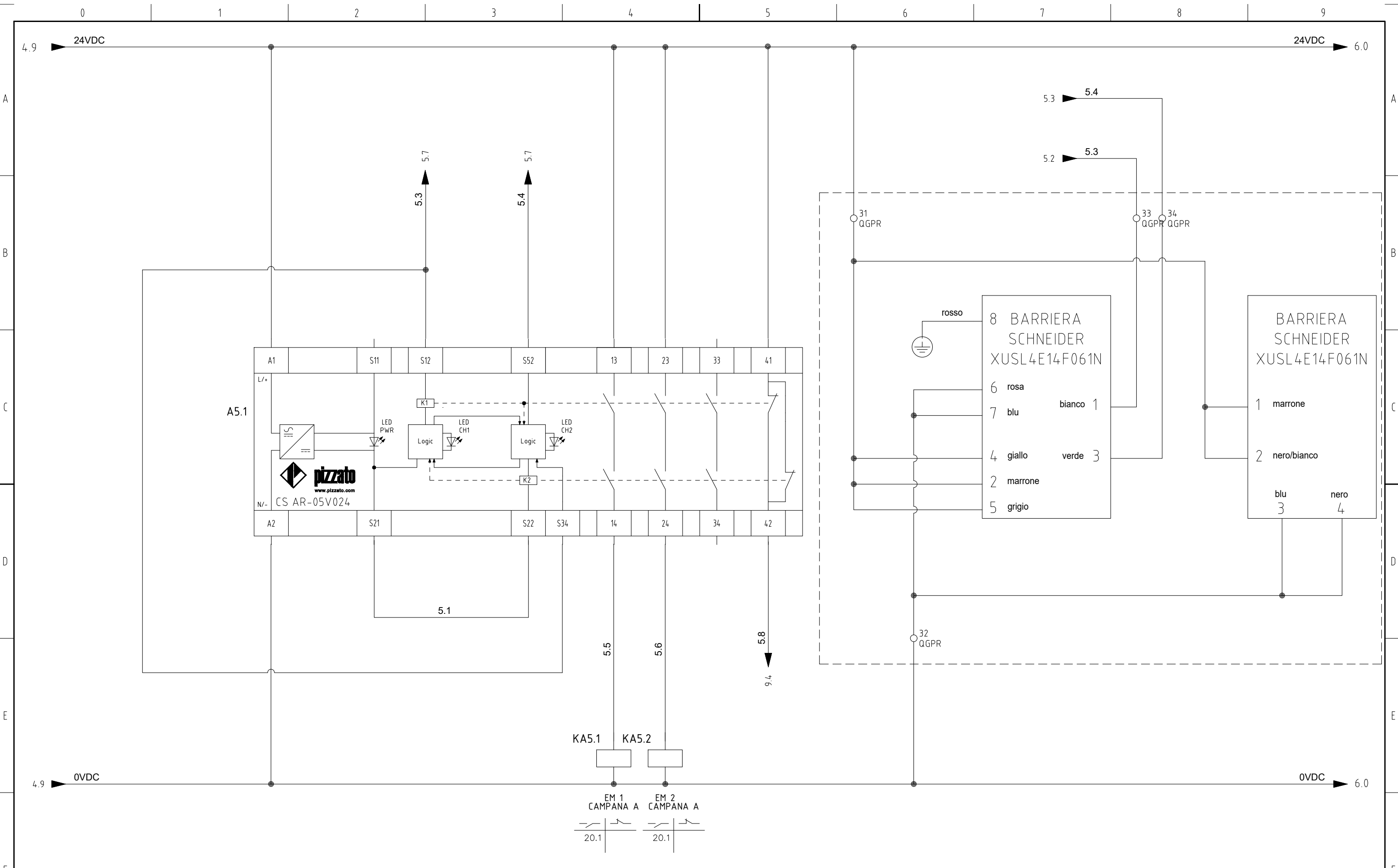


REV.	MODIFICA	DATA	FIRMA	APPR.

DATA 04/07/24
 DISEGN. T.B.
 VISTO
 APPR.

DELTA ENERGY SRL
 Soluzioni Elettriche

FILE :	PDL34	E24Q103	=
	EMERGENZA PORTA A	24Q103	+
			FG. 4
			F.S. 5



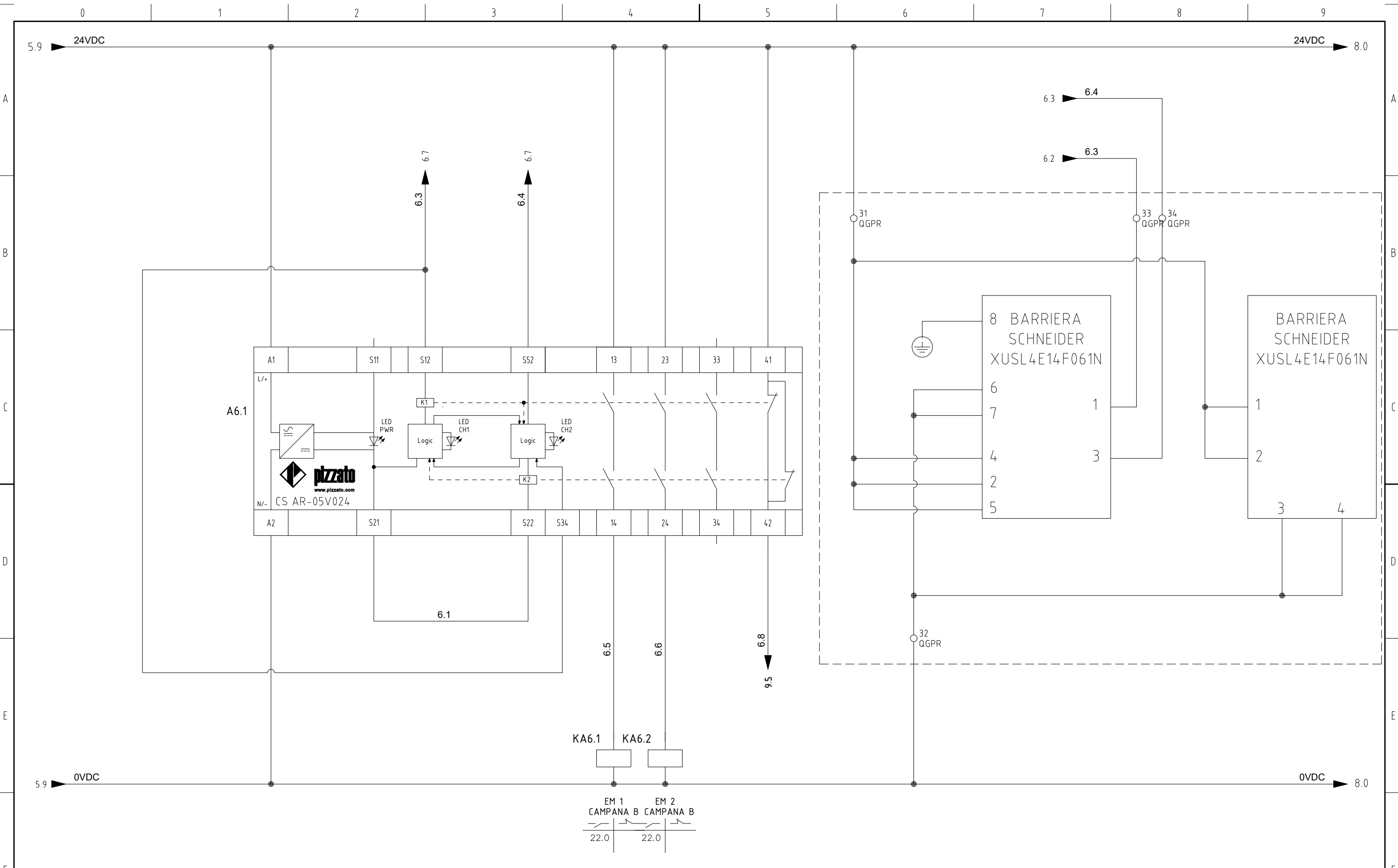
REV.	MODIFICA	DATA	FIRMA	APPR.	DATA	04/07/24
					DISEGN.	T.B.
					VISTO	

DELTA ENERGY SRL
Soluzioni Elettriche

FILE :

PDL34
EMERGENZA BARRIERE

E24Q103	=
24Q103	+
	FG. 5
	F.S. 6



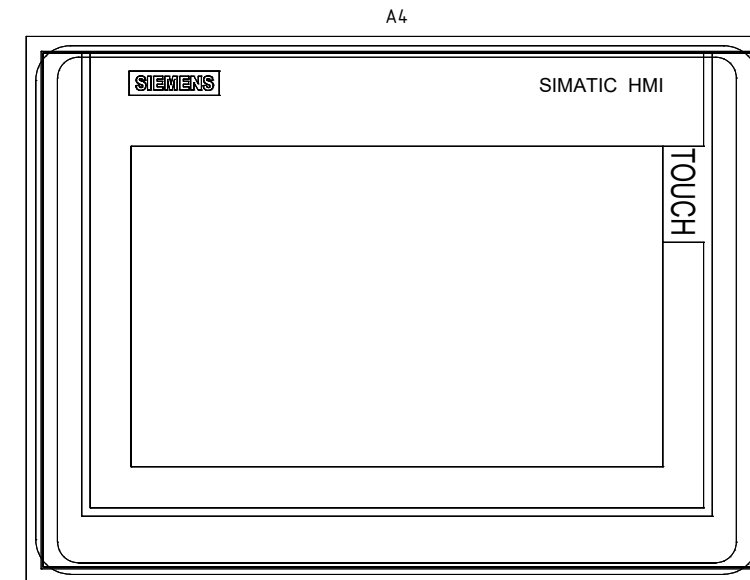
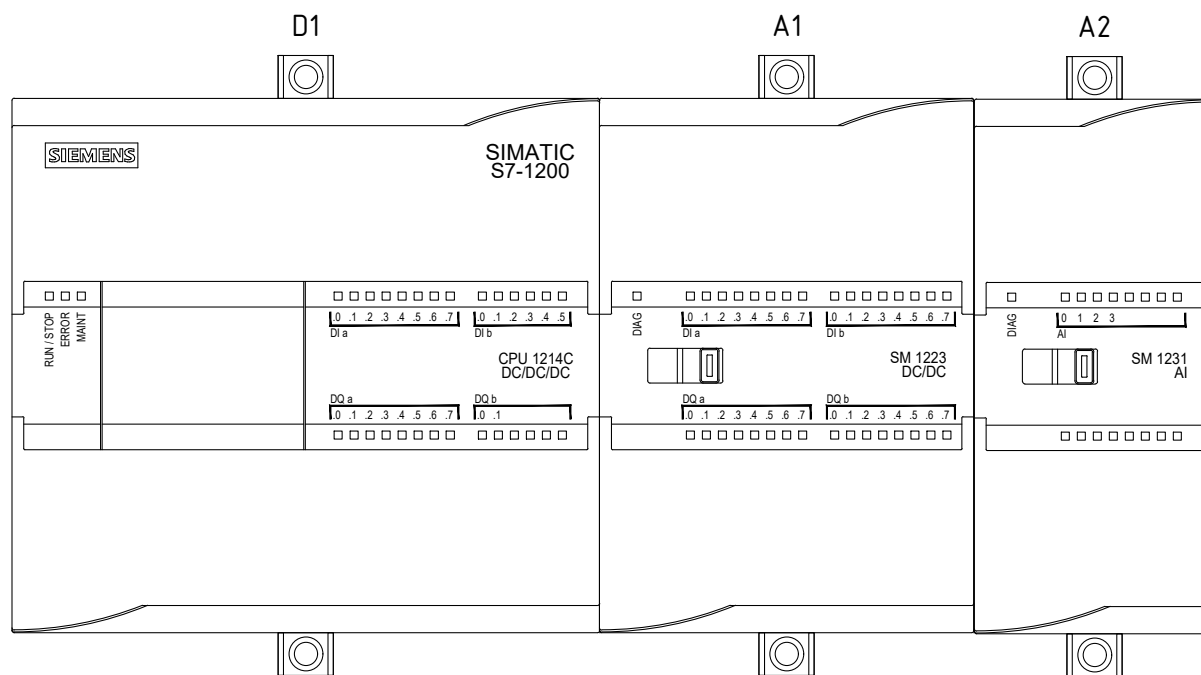
REV.	MODIFICA	DATA	FIRMA	APPR.	DATA
					04/07/24
					T.B.

DELTA 4S ENERGY SRL
Soluzioni Elettriche

FILE :

PDL34
EMERGENZA BARRIERE

E24Q103
24Q103
FG. 6
F.S. 7



REV.	MODIFICA	DATA	FIRMA	APPR.	

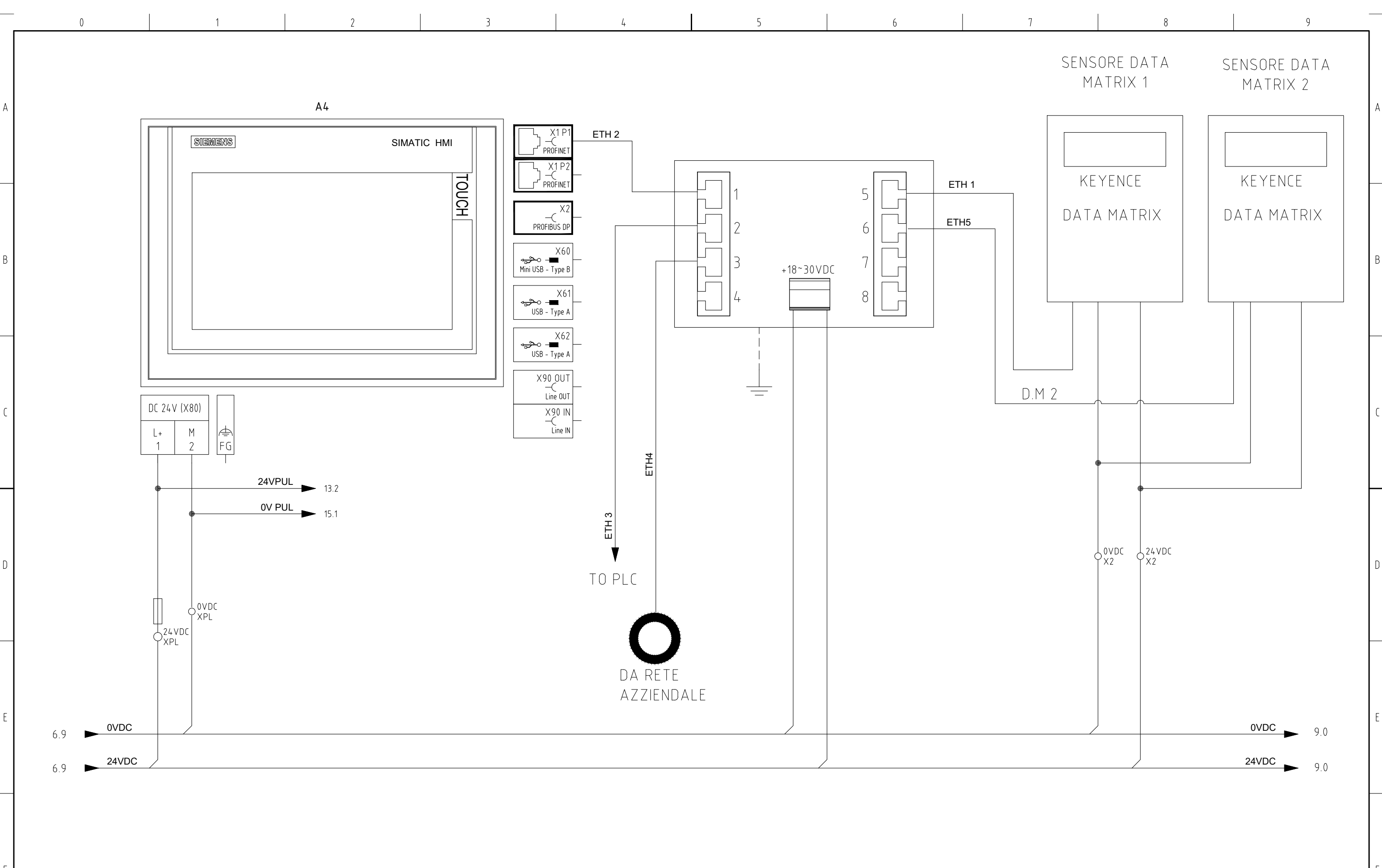
DATA 04/07/24
 DISEGN. T.B.
 VISTO
 APPR.

DELTA 4S YNERGY SRL
 Soluzioni Elettriche

FILE :

PDL34
 LAYOUT PLC

E24Q103	=	
	+	
24Q103		FG. 7
		F.S. 8



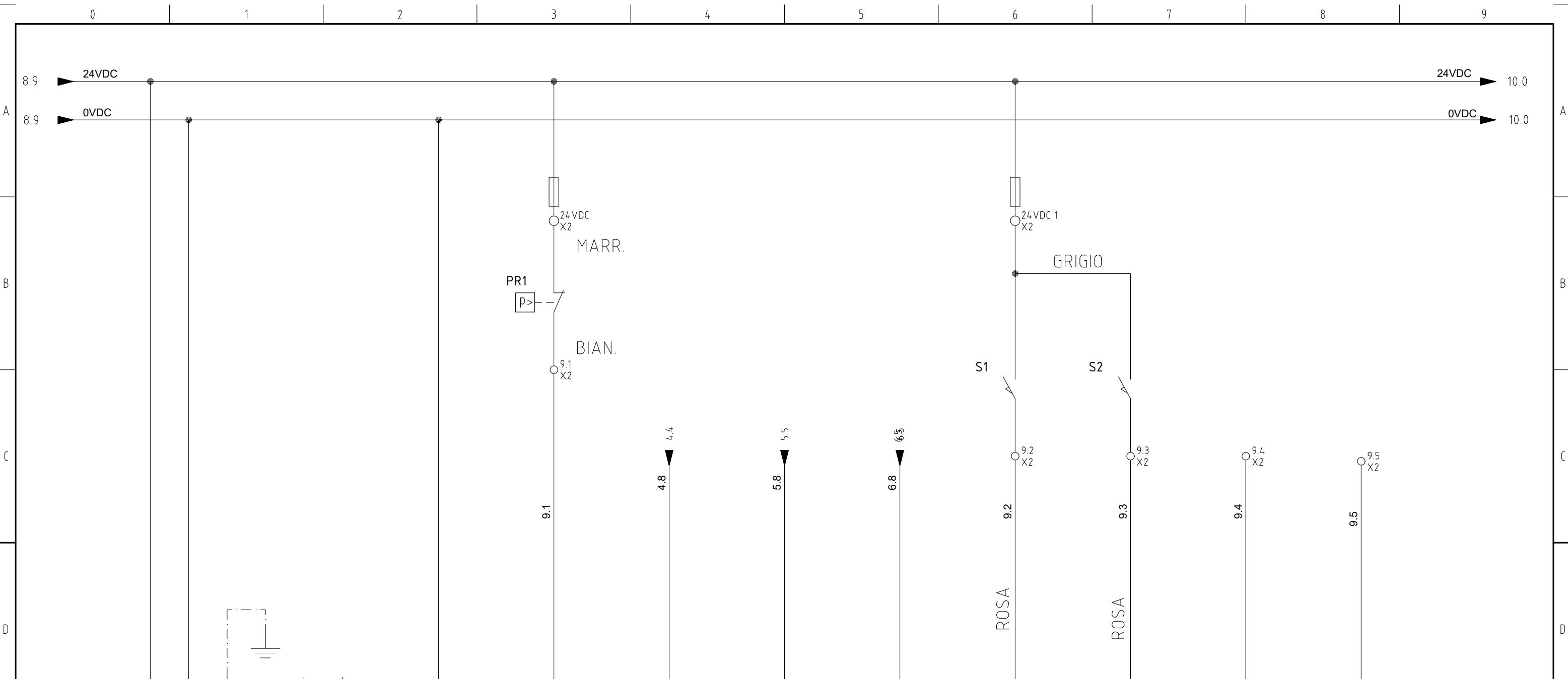
REV.	MODIFICA	DATA	FIRMA	APPR.	

DELTA 4S YNERGY SRL
 Soluzioni Elettriche

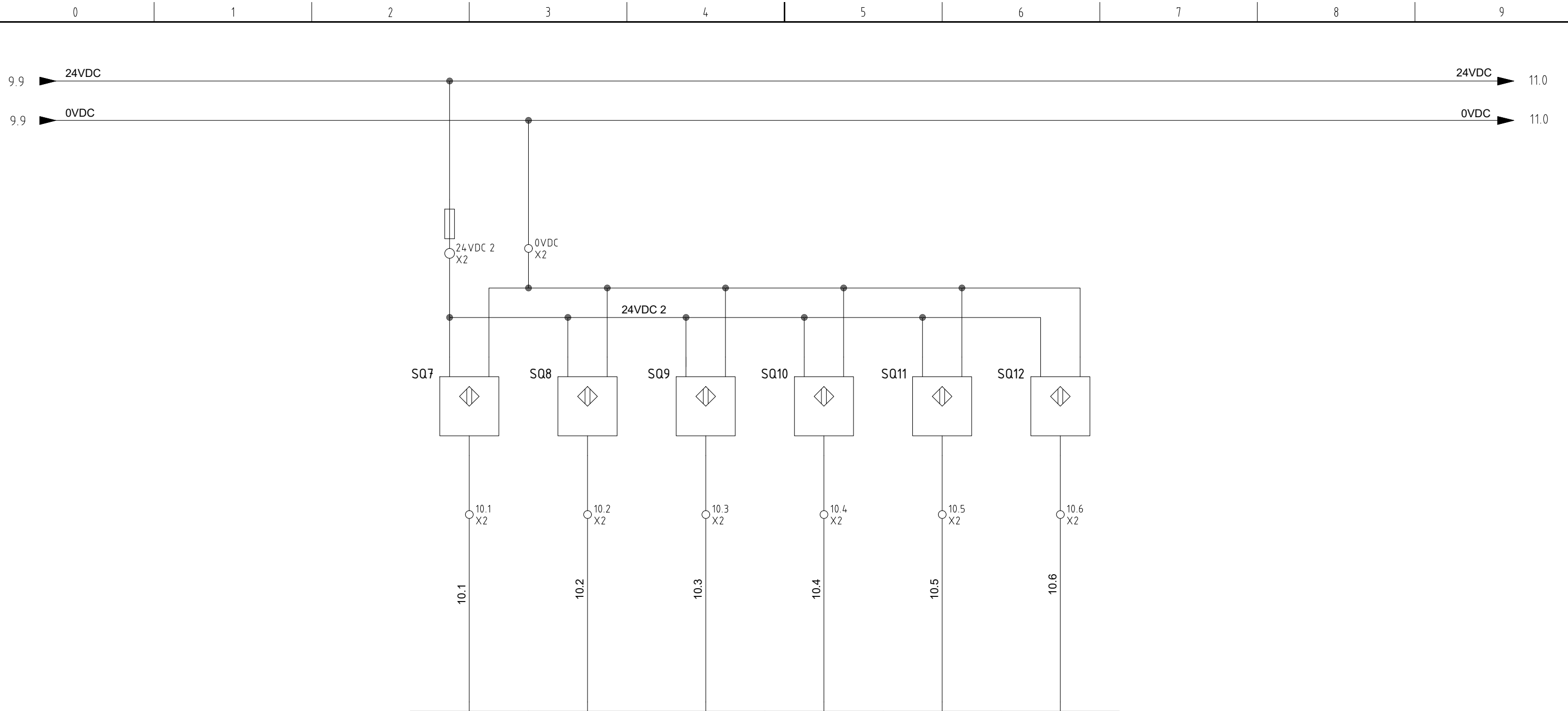
FILE :

PDL34
 DISPLAY

E24Q103	=	
	+	
24Q103	FG.	8
	F.S.	9



X10/1 X10/2 X10/3			X10/4 X10/5		X10/6		X10/7		X10/8		X10/9		X10/10		X10/11		X10/12		X10/13		X10/14		
							10.0		10.1		10.2		10.3		10.4		10.5		10.6		10.7		
CPU: 2G MOD.: 1			CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		
Name: A7.1		Sheet: 7.2	Name: A7.1		Sheet: 7.2	Name: A7.1		Sheet: 7.2	Name: D1		Sheet: 7.1	Name: D1		Sheet: 7.1	Name: D1		Sheet: 7.1	Name: D1		Sheet: 7.1	Name: D1		Sheet: 7.1
							PRESENZA ARIA		MODULO SICUREZZA PULSANTE / PORTE		MODULO SICUREZZA BARRIERA 1		MODULO SICUREZZA BARRIERA 2		SENSORE PORTA 1 APERTA		SENSORE PORTA 2 APERTA		SENSORE PORTA 3 APERTA		SENSORE PORTA 4 APERTA		



D1		X10/15	X10/16	X10/17	X10/18	X10/19	X10/20
		I1.0	I1.1	I1.2	I1.3	I1.4	I1.5
		CPU: 2G MOD.: 1	CPU: 2G MOD.: 1	CPU: 2G MOD.: 1	CPU: 2G MOD.: 1	CPU: 2G MOD.: 1	CPU: 2G MOD.: 1
Name: D1	Sheet: 7.1	Name: D1	Sheet: 7.1	Name: D1	Sheet: 7.1	Name: D1	Sheet: 7.1
		PRESENZA PEZZO CAMPANA A	PRESENZA PEZZO SBAGLIATO CAMPANA A	CL1 + CAMPANA A	CL1 - CAMPANA A	CL2 + CAMPANA A	CL2 - CAMPANA A

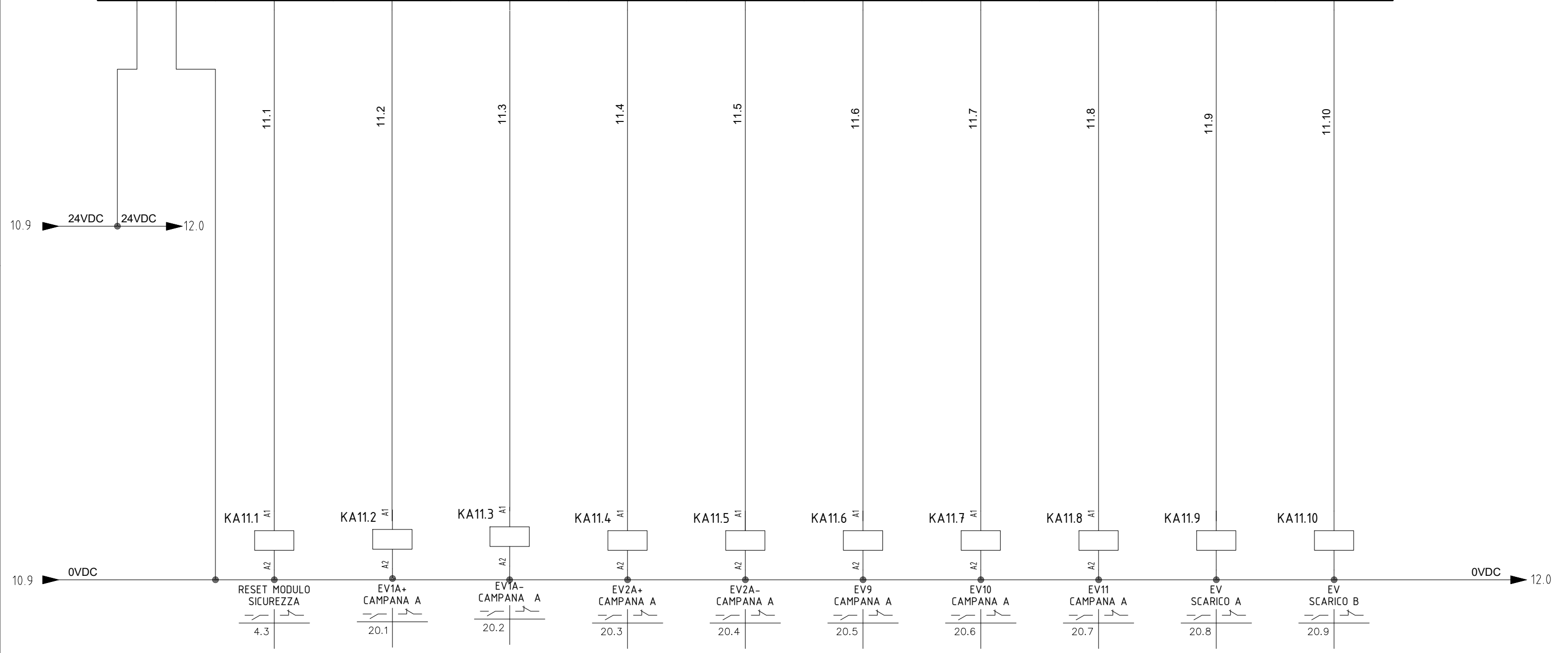
REV.	MODIFICA	DATA	FIRMA	APPR.	DATA	04/07/24
					DISEGN.	T.B.
					VISTO	



FILE :	PDL34	E24Q103	=
	INGRESSI CPU	24Q103	+
			FG. 10
			F.S. 11

D1

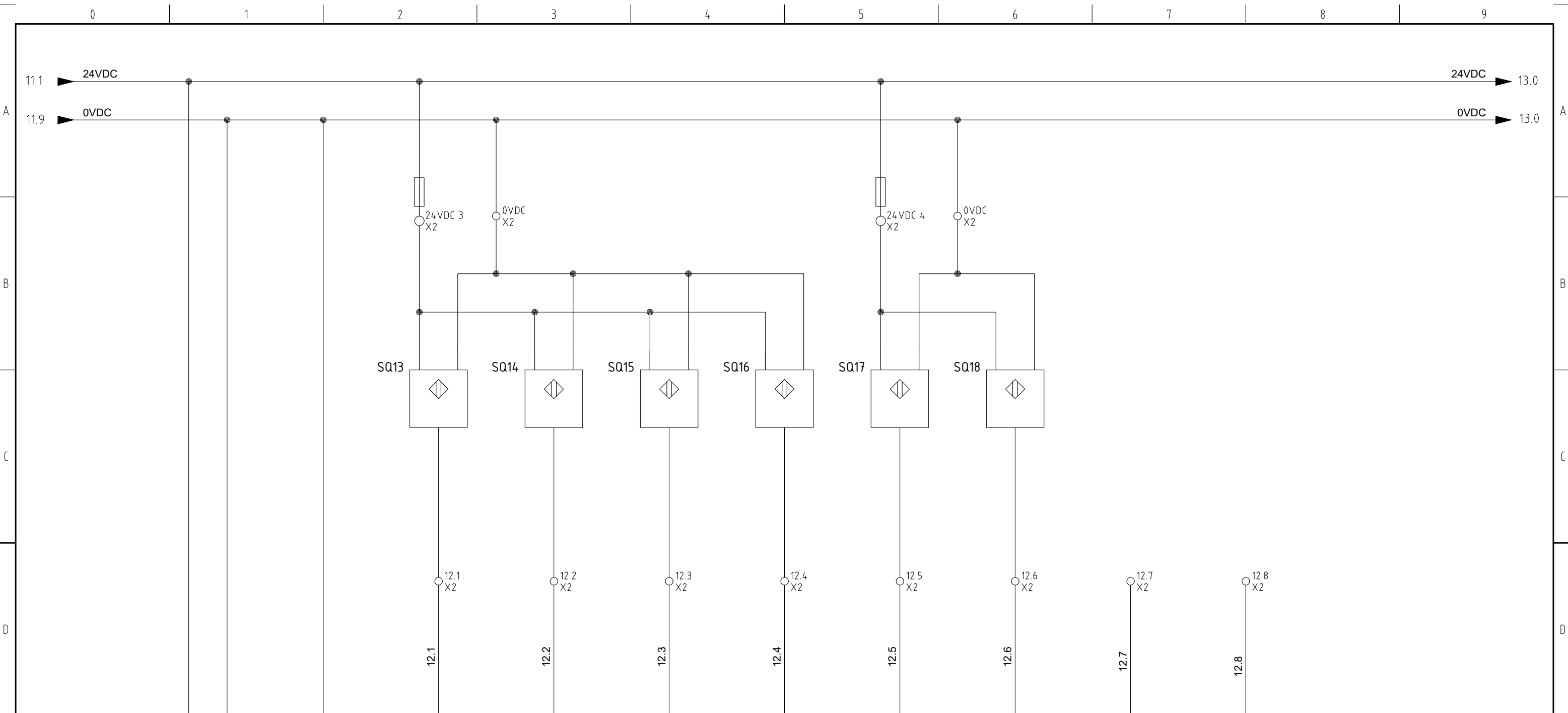
		RESET MODULO SICUREZZA		EV1A+ CAMPANA A		EV1A- CAMPANA A		EV2A+ CAMPANA A		EV2A- CAMPANA A		EV9 CAMPANA A		EV10 CAMPANA A		EV11 CAMPANA A		EV SCARICO A		EV SCARICO B	
Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:
A7.1	7.2	D1	7.1	D1	7.1	D1	7.1	D1	7.1	D1	7.1	D1	7.1	D1	7.1	A7.1	7.2	D1	7.1	D1	7.1
CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		CPU: 2G MOD.: 1		CPU: 2G MOD.: EV7		CPU: 2G MOD.: 1		CPU: 2G MOD.: 1			
X12/1 X12/2		Q0.0 X12/3		Q0.1 X12/4		Q0.2 X12/5		Q0.3 X12/6		Q0.4 X12/7		Q0.5 X12/8		Q0.6 X12/9		Q0.7 X12/10		Q1.0 X12/11		Q1.1 X12/12	



DATA	04/07/24
DISEGN.	T.B.
VISTO	
REV.	MODIFICA
	DATA
	FIRMA
	APPR.



FILE :	PDL34	E24Q103	=
	USCITE CPU	24Q103	+
			FG. 11
			F.S. 12



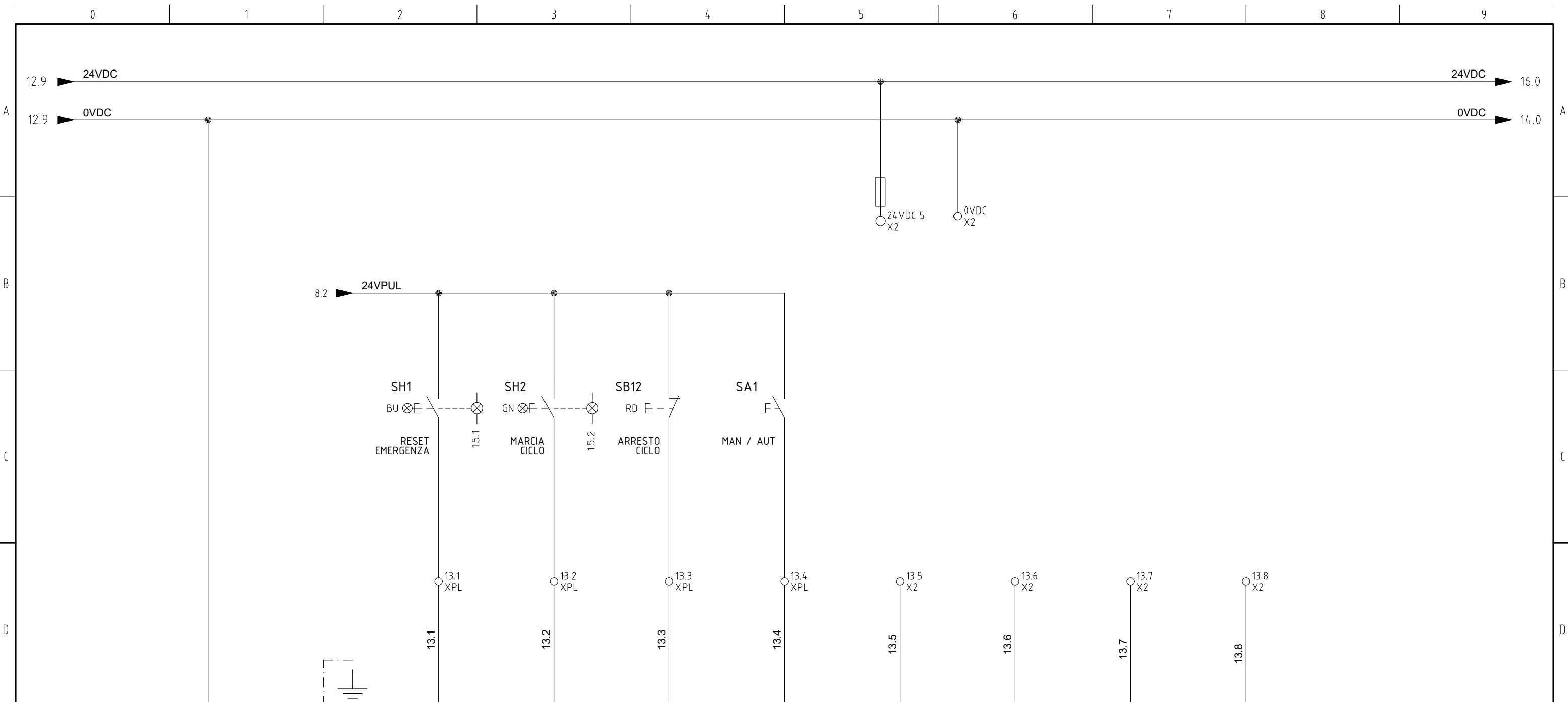
A1

X10/1 X10/2		X10/3		X10/4		X10/5		X10/6		X10/7		X10/8		X10/9		X10/10		X10/11	
				12.0		12.1		12.2		12.3		12.4		12.5		12.6		12.7	
CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0	
Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4
				PRESENZA PEZZO CAMPANA B		PRESENZA PEZZO SBAGLIATO CAMPANA B		CL1+ CAMPANA B		CL1- CAMPANA B		CL2 + CAMPANA B		CL2 - CAMPANA B		SCORTA		SCORTA	

REV.	MODIFICA	DATA	FIRMA	APPR.	DATA	04/07/24
					DISEGN.	T.B.
					VISTO	



FILE :	PDL34	E24Q103	=
	INGRESSI A1	24Q103	+
			FG. 12
			F.S. 13



X11/1		X11/3		X11/4		X11/5		X11/6		X11/7		X11/8		X11/9		X11/10		X11/11	
				13.0		13.1		13.2		13.3		13.4		13.5		13.6		13.7	
CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0	
Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:	Name:	Sheet:
A7.2	7.4	A7.2	7.4	A7.2	7.4	A7.2	7.4	A7.2	7.4	A7.2	7.4	A7.2	7.4	A7.2	7.4	A7.2	7.4	A7.2	7.4

REV.	MODIFICA	DATA	FIRMA	APPR.	DATA	04/07/24
					DISEGN.	T.B.
					VISTO	



FILE :	PDL34	E24Q103	=
	INGRESSI A1	24Q103	+
			FG. 13
			F.S. 14

A

B

C

D

E

F

A1

EV1 + CAMPANA B		EV1 - CAMPANA B		EV2 + CAMPANA B		EV2 - CAMPANA B		EV9 CAMPANA B		EV10 CAMPANA B		EV11 CAMPANA B		SCORTA	
Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4
CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0	
Q2.0		Q2.1		Q2.2		Q2.3		Q2.4		Q2.5		Q2.6		Q2.7	
X12/4		X12/5		X12/6		X12/7		X12/8		X12/9		X12/10		X12/11	

14.1

14.2

14.3

14.4

14.5

14.6

14.7

14.8

KA14.1

KA14.2

KA14.3

KA14.4

KA14.5

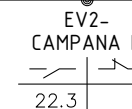
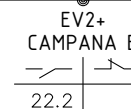
KA14.6

KA14.7

KA14.8

13.0 0VDC

0VDC 15.0



SCORTA

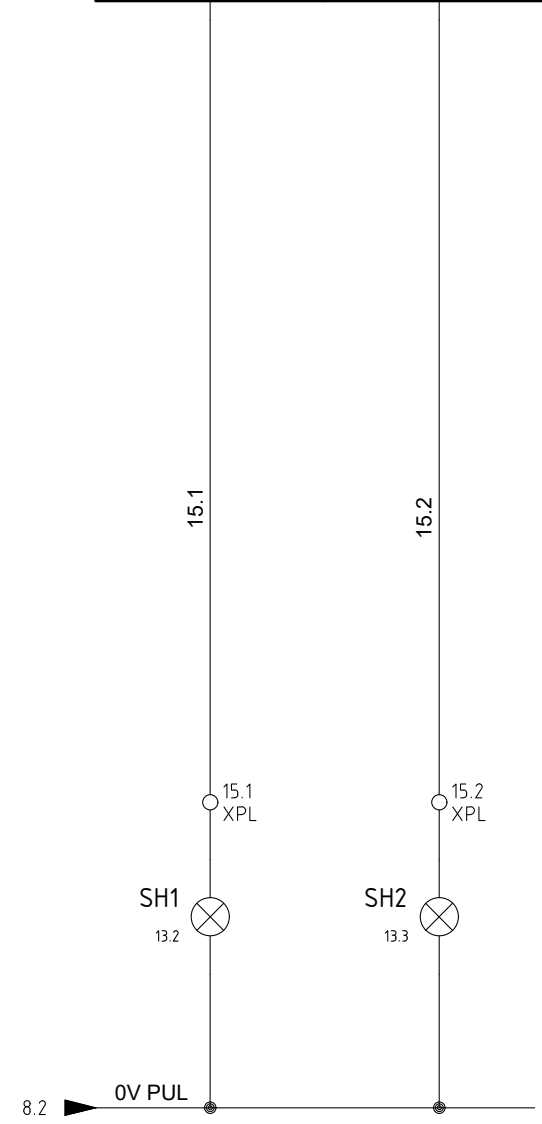
REV.	MODIFICA	DATA	FIRMA	APPR.	DATA	04/07/24
					DISEGN.	T.B.
					VISTO	



FILE :	PDL34	E24Q103	=
	USCITE A1	24Q103	+
			FG. 14
			F.S. 15

A1

LED PULSANTE RESET EMERGENZA		LED PULSANTE START CICLO		LED SPIA ALLARME									
Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4	Name: A7.2	Sheet: 7.4
CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0		CPU: 2G MOD.: 0	
Q3.0		Q3.1		Q3.2		Q3.3		Q3.4		Q3.5		Q3.6	
X13/4		X13/5		X13/6		X13/7		X13/8		X13/9		X13/10	



REV.	MODIFICA	DATA	FIRMA	APPR.	

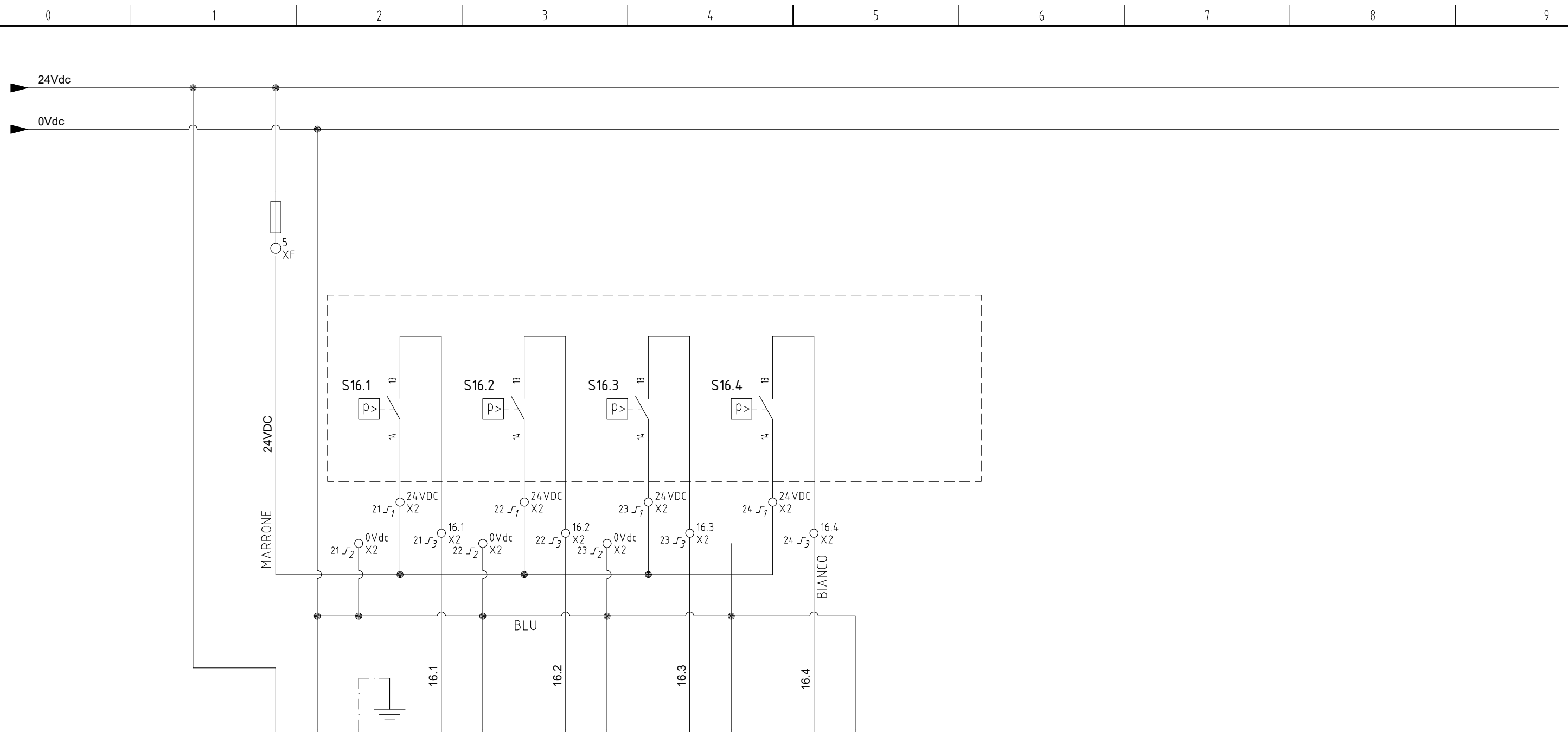
DATA 04/07/24
 DISEGN. T.B.
 VISTO
 APPR.



FILE :

PDL34
 USCITE A1

E24Q103	=
	+
24Q103	FG. 15 F.S. 16

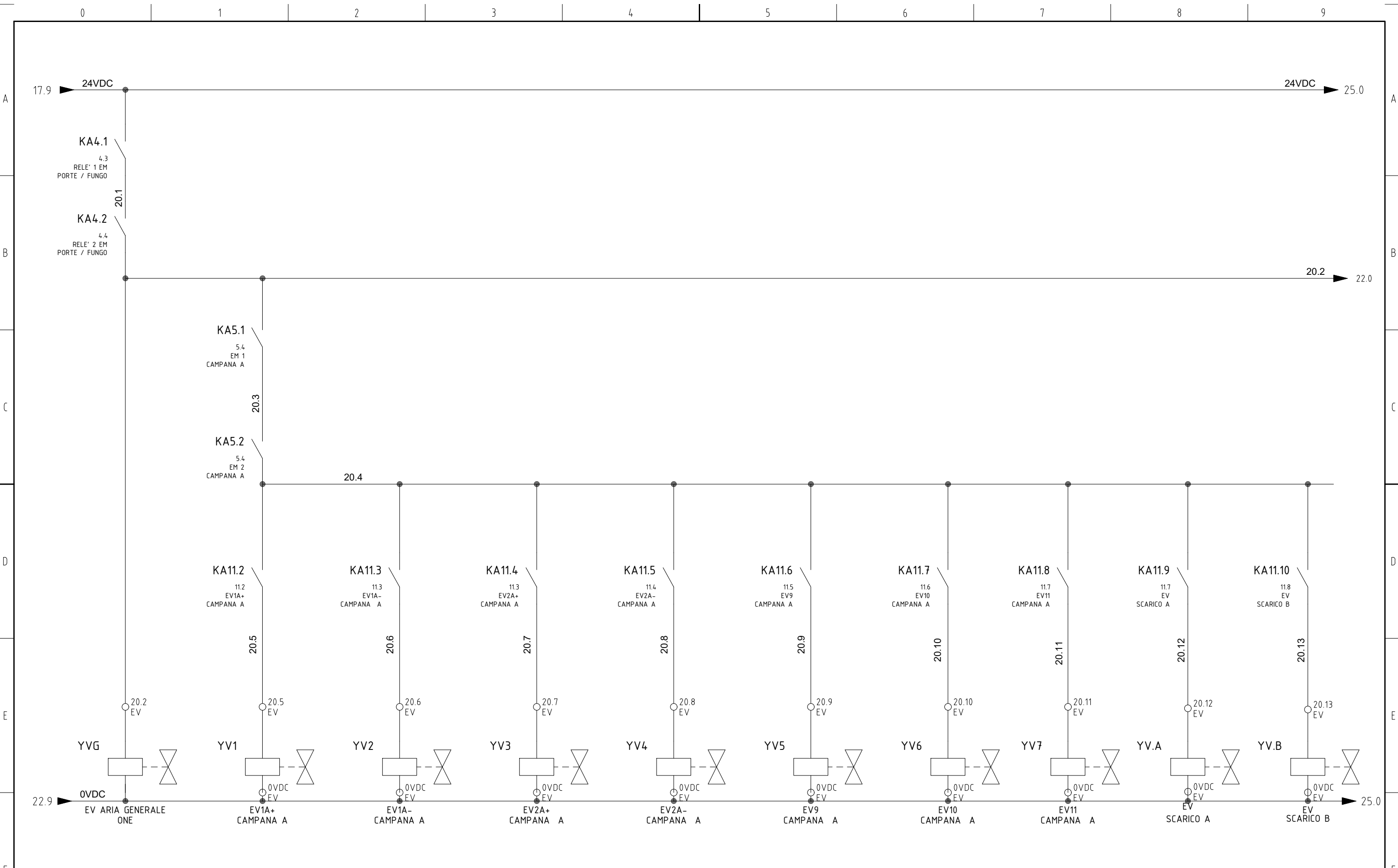


X10/1 X10/2 X10/3			X10/4 X10/5		X10/6 X10/7		X11/4 X11/5		X11/6 X11/7	
CPU: 2G MOD.: 4			CPU: 2G MOD.: 4		CPU: 2G MOD.: 4		CPU: 2G MOD.: 4		CPU: 2G MOD.: 4	
Name: A7.3	Sheet: 7.4D		Name: A2	Sheet: 7.4	Name: A2	Sheet: 7.4	Name: A2	Sheet: 7.4	Name: A2	Sheet: 7.4
			PRESSOSTATO TR1		PRESSOSTATO TR2		PRESSOSTATO TR3		PRESSOSTATO TR4	

REV.	MODIFICA	DATA	FIRMA	APPR.	DATA	04/07/24
					DISEGN.	T.B.
					VISTO	



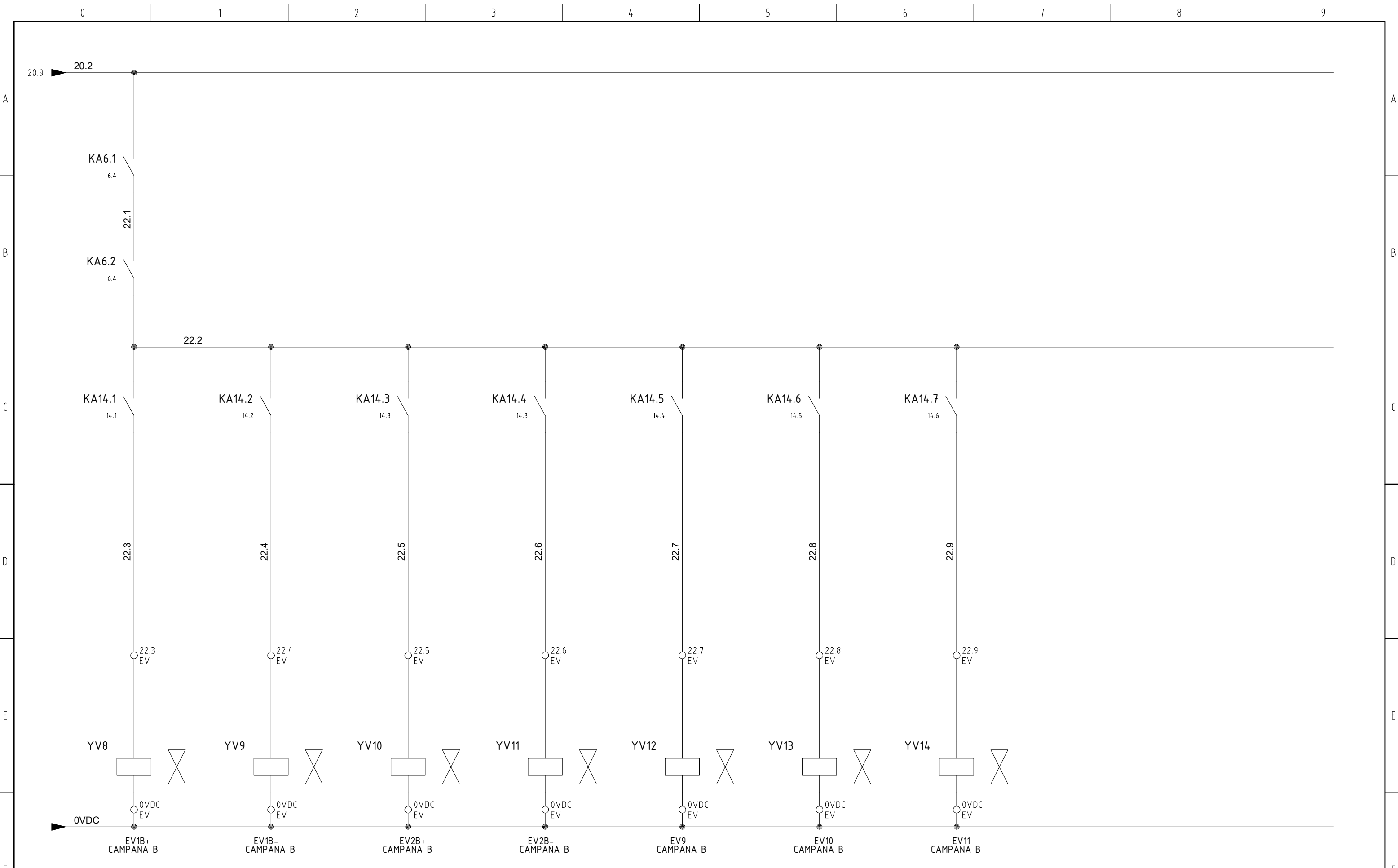
FILE :	PDL34	E24Q103	=
	INGRESSI ANALOGICI	24Q103	+
			FG. 16
			F.S. 20



DATA	04/07/24
DISEGN.	T.B.
VISTO	
APPR.	
REV.	MODIFICA
	DATA
	FIRMA



FILE :	PDL34	E24Q103	=
	EV CAMPANA A	24Q103	+
			FG. 20
			F.S. 22



REV.	MODIFICA	DATA	FIRMA	APPR.	

DATA 04/07/24
 DISEGN. T.B.
 VISTO
 APPR.



FILE :

PDL34
EV CAMPANA B

E24Q103	=	
	+	
24Q103	FG. 22	
	F.S. /	